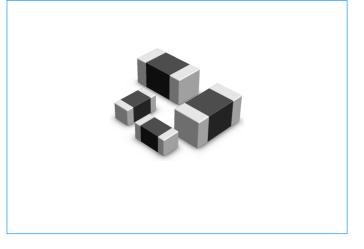




SV0201E5R5G0B

Description

The SV0201E5R5G0B is based on Multilayer fabrication technology. These components are designed to suppress a variety of transient events, including those specified in IEC 61000-4-2 or other standards used for Electromagnetic Compliance (EMC). The SV0201E5R5G0B is typically applied to protect integrated circuits and other components at the circuit board level. It can operate over a wider temperature range than zener diodes.



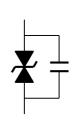
Features

- **u** SMD type zinc oxide based ceramic chip
- u Lead free plating termination provided good solderability characteristic
- Insulator overcoat keeps excellent low and stable leakage current
- u Quick response time (<1ns)
- u Low clamping voltage
- u High transient current capability
- u Meet IEC 61000-4-2 standard
- u Compact size for EIA 0201

Applications

- Application for Mother Board, Notebook, Cellular Phone, PDA, handheld device, DSC, DV, Scanner, and Set-Top Box...etc.
- **u** Suitable for Push-Button, Power Line and Low Frequency single line over-voltage protect.

Equivalent Circuits



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SV0201E5R5G0B

Electrical Characteristics (25±5℃)

Symbol	Minimum	Typical	Maximum	Units
Vrms	_	_	4	V
V _{DC}	—	_	5.5	V
Vv	8	_	14	V
Vc	—	_	31	V
СР	_	10	_	pF

V_{RMS} - Maximum AC operating voltage the varistor can maintain and not exceed 10µA leakage current.

 V_{DC} - Maximum DC operating voltage the varistor can maintain and not exceed 10µA leakage current.

 $V_{V}\,$ - Voltage across the device measure at 1mA DC current.

Equivalent to V_B "breakdown voltage".

 $V_{C}\,$ - Maximum peak current across the varistor with 8/20 μs waveform and 1A pulse current.

C_P - Device capacitance measured with zero volt bias 1Vrms at 1MHz.

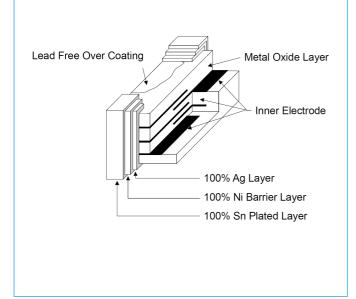


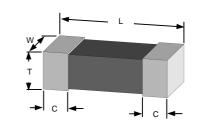
BHF RoHS

Surface Mount Multilayer Varistor

SV0201E5R5G0B

Construction & Dimensions





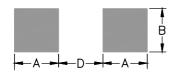
Size EIA	0201	
Symbol	Inches	Millimeters
L	0.024±0.002	0.60±0.05
w	0.012±0.002	0.30±0.05
Т	0.012±0.002	0.30±0.05
С	0.008±0.004	0.20±0.10

Pad Layouts & Precaution for handling of substrate

Solder cream in reflow soldering

Refer to the recommendable land pattern as printing mask pattern for solder cream.

(1) Print solder in a thickness of 150 to 200µm



Size EIA	0201	
Symbol	Millimeters	
Α	0.25±0.05	
B 0.3±0.05		
D	0.3±0.05	

Precaution for handling of substrate

Do not exceed to bend the board after soldering thes product extremely. (reference examples)

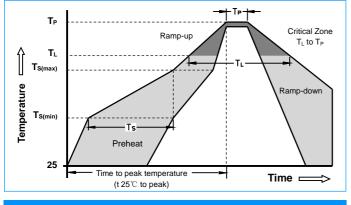
- I Mounting place must be as far as possible from the position, which is close to the break line of board or on the line of large holes of board.
- I Do not bend extremely the board, in mounting another component. If necessary, use back-up pin (support pin) to prevent from bending extremely.
- I Do not break the board by hand. We recommend to use the machine or the jig to break it.

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SV0201E5R5G0B

Soldering Parameters



Precaution for Soldering

Note that this product will be easily damaged by rapid heating, rapid cooling or local heating.

Do not give heat shock over 100 $^\circ\!C\,$ in the process of soldering. We recommend to take preheating and gradual cooling

Soldering gun procedure

Note the follows, in case of using solder gun for replacement. 1) The tip temperature must be less than 280 for the period within 3 seconds by using soldering gun under 30W 2) The soldering gun tip shall not touch this product directly.

Soldering volume

Note that excess of soldering volume will easily get crack the body of this product.

Reflow Condition		Pb-Free assembly
	-Temperature Min (T _{s(min)})	+150℃
Pre Heat	-Temperature Max (T _{s(max)})	+200 ℃
	-Time (min to max) (T _s)	60 -180 Seconds
Average ra to peak	amp up rate (Liquidus Temp T _L)	3℃/Second Max
T _{S(max)} to T	∟ - Ramp-up Rate	3℃/Second Max
Reflow	- Temperature (T _L) (Liquidus)	+217℃
	- Time (min to max) (T∟)	60 -150 Seconds
Peak Tem	perature (T _P)	260 ℃
Time within 5 $^{\rm C}$ of actual peak Temperature (T_P)		20-40 Seconds
Ramp-down Rate		6℃/Second Max
Time 25 $^\circ\!\!\!\!\!^{\circ}$ to peak Temperature (TP)		8 minutes Max

General Technical Data			
Operating Temperature		-40°C~ +85°C	
Storage Temperature		-40°C~ +85°C	
Response Time		<1 ns	
Solderability		245±5℃, 3±1sec	
Solder leach resista	ince	260±5℃, 10±1sec	
Taping Package Storage Condition	Storage Temperature	5℃ ~ 40℃	
	Relative Humidity	To 65%	
	Storage Time	12 Months max	

Environmental Performance			
Item	Specifications	Test Condition	
Bias Humidity	$\triangle V_V / V_V \le \pm 10 \%$	90%RH, 40℃, Working Voltage, 1000 hrs	
Thermal Shock	$\triangle V_V / V_V \le \pm 10 \%$	-40 $^\circ\!\!\mathrm{C}$ to 85 $^\circ\!\!\mathrm{C}$, 30 min. cycle, 5 cycles	
Full Load Voltage	$\triangle V_V / V_V \le \pm 10 \%$	Working Voltage, 85℃,1000 hrs	

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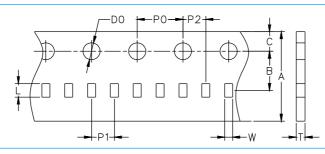




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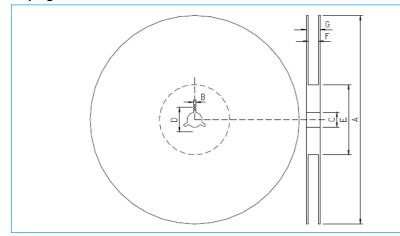
Packaging Information

Carrier Tape Dimensions



Size EIA	0201		
Symbol	Inches	Millimeters	
A	0.315±0.004	8.00±0.10	
В	0.138±0.002	3.50±0.05	
C	0.069±0.002	1.75±0.05	
D0	0.061±0.002	1.55±0.05	
P0	0.157±0.004	4.00±0.10	
P1	0.079±0.002	2.00±0.05	
P2	0.079±0.002	2.00±0.05	
W	0.014±0.001	0.36±0.02	
L	0.028±0.001	0.70±0.02	
т	0.017±0.001	0.42±0.02	

Taping Reel Dimensions



Symbol	Unit: Millimeters	
Α	178.0±2.0	
В	2.0±0.5	
С	C 13.0±0.5	
D	21.0±0.8	
E	62.0±1.5	
F	9.0±0.5	
G	G 13.0±1.0	

Taping Specifications

There Shall be the portion having no product in both the head and the end of taping, and there shall be the cover tape in the head of taping.

Quantity of Products in the Taping Package

SIZE EIA	0201
Standard Packing Quantity (PCS / REEL)	15,000

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